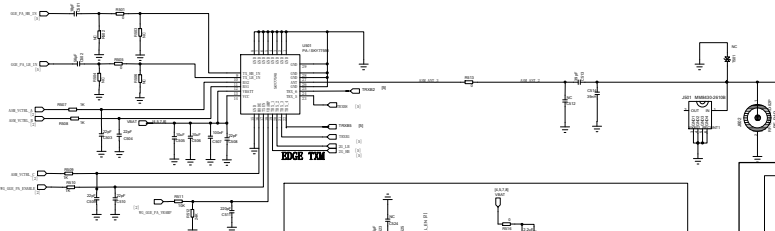
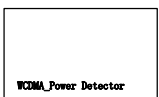
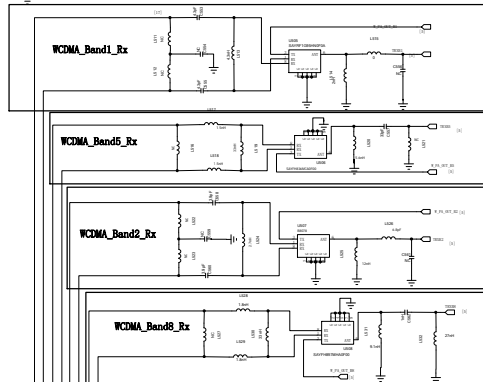
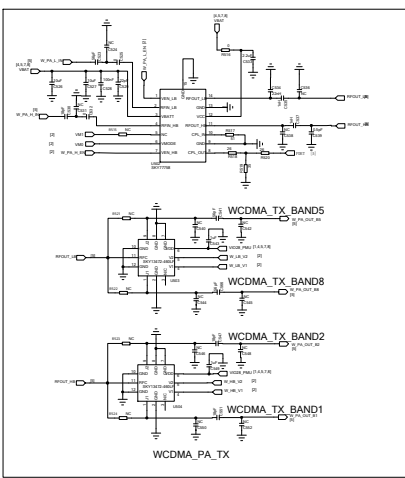
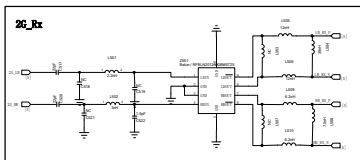


SKY7190 control logic table Enable Vcc Vcc1 Vcc2

| | | | |
|------------|---|---|---|
| LR_OSC_TX | H | L | H |
| HR_OSC_TX | H | L | H |
| LR_EDGE_TX | H | H | H |
| HR_EDGE_TX | H | H | H |
| TS1 | L | H | L |
| TS2 | L | H | L |
| TS3 | L | H | L |
| TS4 | L | H | L |
| TS5 | L | H | L |
| TS6 | L | L | H |



Antenna matching depends on antenna design



Two Agglutination Circuit Conditions:
 1. TSI Circuit : 2800-TSX, 8055-8050-WC, 8054+100E+1A, 8056-8057-0cm
 2. XTAL Circuit : 2800-Mobile XTAL, 8053-8050-0cm, 8054+8055-8057-WC

Route **MIKADC_RSF**/**MIKADC_TSI** as differential traces with well-GND shielding and route **MIKADC_QD** with shield traces with under **MIKADC_TSI**/**MIKADC_RSF** traces to provide return current path.

