

VISHAY INTERTECHNOLOGY, INC.

LINEAR FILM RESISTORS

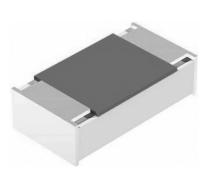
Pulse Load Handling for Fixed Linear Resistors



The power and thermal behavior of fixed linear resistors are mostly based on DC or RMS loads, but pulse loads, like single energy pulse or a continuous flow of pulses, become more and more an important factor in professional electronics. Pulse load situations for film resistors are not allowed to exceed the following limits:

- Single pulses have to be limited in peak power for a given pulse duration.
- The average power load P of the continuous pulse load shall not exceed the rated power dissipation P_{70} .
- The maximum amplitude of pulse voltage, and single and continuous pulses, has to be limited for high ohmic values

1. Single Pulse Load



The maximum permitted single peak power of the specified resistor is shown in the pulse load diagrams of our Vishay Draloric Beyschlag data sheets as a function of pulse duration time. All these diagrams are based on rectangular single pulse shape.

For different pulse shapes, the energy of a single pulse has to be calculated.

$$W = \int_{t_1}^{t_2} P_{(t)} \bullet dt$$

The result of the energy has to be compared with a similar rectangular pulse shape. The power amplitudes of the real single pulse and the similar rectangular pulse must be equal.

$$\hat{P} = \hat{P}_{rec - cal}$$

Now it is easy to calculate the appropriated duration time of the rectangular single pulse.

$$W = W_{rec - cal} = \widehat{P}_{rec - cal} \bullet t_{rec - cal}$$



The individual single pulse is now transformed into a similar rectangular pulse shape which can be compared with the single pulse diagrams of the resistor data sheet.

If the amplitude of the power is lower than the maximum permitted peak power of the resistor, then the permitted pulse duration can be read out of the single pulse diagram. If the calculated rectangular duration time is lower or equal to the maximum permitted pulse duration out of the data sheet, then the resistor is suitable for the application.

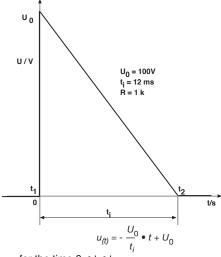
$$W = W_{rec - cal} = \hat{P}_{rec - cal} \bullet t_{rec - cal} \le W_{Diagram}$$

Linear Resistors

Vishay



The following example is a calculation for a professional MINI-MELF resistor MMA 0204 for a triangle pulse shape:



for the time $0 \le t \le t_i$

Energy of a triangle voltage pulse:

$$W = \int_{t_1}^{t_2} P_{(t)} \cdot dt$$

$$W = \frac{1}{R} \int_{t_1}^{t_2} u^2_{(t)} \cdot dt$$

$$W = \frac{U_0^2}{R} \cdot \frac{t_i}{3}$$

$$W = \widehat{p} \bullet \frac{t_i}{3}$$

with a peak power of:

$$\hat{p} = \hat{p}_{rec - cal} = \frac{U_0^2}{R}$$

$$\hat{p} = 10W$$

and compared with a similar rectangular pulse:

$$W = W_{\text{rec - cal}} = \hat{P}_{\text{rec - cal}} \bullet t_{\text{rec - cal}}$$

in a similar pulse duration time:

$$t_{rec - cal} = \frac{t_i}{3}$$

$$t_{rec - cal} = \frac{12ms}{3}$$





Vishay

Figure 1 shows that the peak power \hat{p} is lower than the maximum permitted peak power of a professional MMA 0204 (up to 30 W). With a peak power \hat{p} of 10 W the diagram shows that the maximum permitted duration $t_{rec-data}$ is around 7 ms. Based on the calculation above, the calculated rectangular pulse duration $t_{rec-cat}$ is lower than the maximum permitted pulse duration $t_{rec-data}$.

A professional MINI-MELF resistor MMA 0204 with a resistance value of 1 k Ω is suitable for this pulse load application.

Single pulses with short duration times (t_i < 200 ~ 300 µs) require resistors which are able to withstand the pulse energy without a permitted change of resistance value or a breakdown. The heat flow out of the resistive layer is very slow compared to the pulse duration, so the maximum permitted peak power reaches a constant value for short duration times.

The capability of similar resistor styles to withstand single energy pulses is dependent on the resistive technology. For extreme pulse load conditions Vishay carbon film, metal glaze, and wire wound resistors are preferrable.

With increasing duration times the maximum peak power becomes more and more equal to the rated power dissipation. This is based on the effect that the heat penetrates more into the ceramic and least into the printed circuit board and environment.

The professional thin film resistor families offer in combination with excellent long-term stability, low temperature coefficient, high reliability, low current noise, and extended pulse load capability.

The permissible single pulse load for thin film resistors leads to a specified resistance change - different to other pulse load tests, e.g. fail to open-circuit. This specified resistance change is given for the extended endurance test (film temperature at $125\,^{\circ}$ C) after 8,000 hours.

Single Pulse

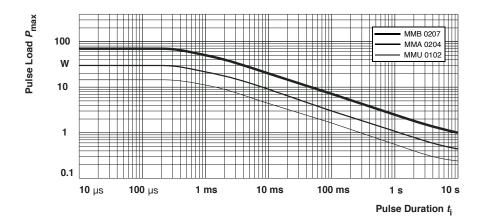


Figure 1. Single pulse diagram of thin film MELF resistors

2. Continuous Pulse Load

The average value *P* of a continuous pulse load has to be calculated by the following equation:

$$P = \bar{p} = \frac{1}{T} \int_{0}^{T} p_{(t)} \bullet dt \le P_{70}$$

For resistance values above the critical resistance R_{crit} the rated power dissipation is given by the resistance value and the limiting element voltage:

$$R_n \ge R_{crit} = \frac{U_{max}^2}{P_{70}}$$
$$P_{70} = \frac{U_{max}^2}{R_n}$$

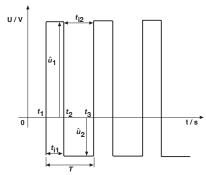
Linear Resistors

Vishay



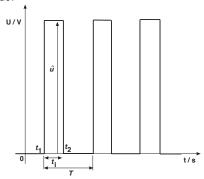
2.1. For Rectangular Continuous Pulses the Average Power of the Pulse Calculates to:

Rectangular pulse with alternating voltage amplitude:



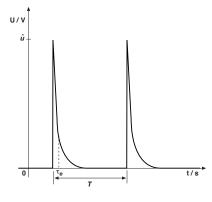
$$P_{rec} = \frac{1}{T} \bullet_{R}^{1} \bullet (\hat{u}_{1}^{2} \bullet t_{j1} \bullet \hat{u}_{2}^{2} \bullet t_{j2})$$
with $t_{j1} = t_{2} - t_{1}$
and $t_{j2} = t_{3} - t_{2}$

Rectangular pulse with positive voltage amplitude:



$$P_{rec} = \frac{t_i}{T} \cdot \hat{P} = \frac{t_i}{T} \cdot \frac{\hat{u}^2}{R}$$
with $t_i = t_2 - t_1$

2.2. For Exponential Continuous Pulses the Average Power of the Pulse Calculates to:



$$\begin{split} P_{e} &= \frac{1}{T} \bullet \frac{\tau_{e}}{2} \bullet \hat{p} = \frac{1}{T} \bullet \frac{\tau_{e}}{2} \bullet \frac{\hat{u}^{2}}{R} \\ & \text{with } \tau_{e} = R \bullet C \text{ or } \tau_{e} = \frac{L}{R} \end{split}$$



2.3. Pulse Load Diagrams

The diagrams for continuous pulse loads show the maximum rated peak pulse load for a rectangular pulse shape with positive voltage amplitude. The equation for rectangular pulses with positive amplitude shows that power dissipation is dependent not only on peak power. It is also dependent on duration time t_i and period T.

$$P_{rec} = \frac{t_i}{T} \cdot \hat{p}$$

To create readable diagrams, the inverse ratio of duration and time period must be limited. The dotted line is the peak power limitation for single power pulses.

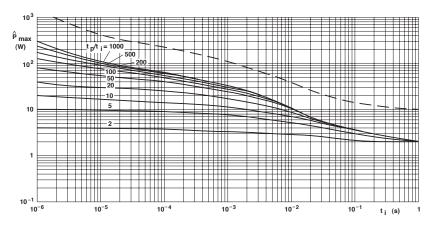


Figure 2. Pulse on a regular basis; maximum permissible peak pulse power as a function of pulse duration (t)

A further possibility to show the peak power capability for continuous pulses is to leave the ration of period T and duration time t_i and to show only the maximum permitted peak power for a given duration time t_i without exceeding the rated power dissipation P_{70} .

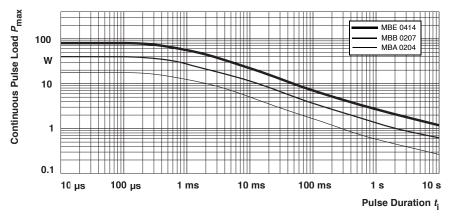


Figure 3. Maximum pulse load, continuous pulses; for permissible resistance change equivalent to 8,000 h operation.

Designers have to calculate the rated frequency by the following equation and compare the result with the frequency of the application. If the frequency of the application is lower than or equal to the rated frequency, the resistor is suitable for the application.

$$P_{rec} = \frac{t_i}{T} \bullet \hat{p} \le P_{70}$$

$$f_{rated} = \frac{1}{t_i} \bullet \frac{\hat{p}}{P_{70}}$$

$$f_{appl} \le f_{rated}$$

Other pulse shapes, like triangular or exponential continuous pulses, can be transferred into similar rectangular pulses under the same conditions as mentioned for single pulses. The results of these calculations can be compared with the data diagrams in the data sheets.

For thin film resistors the permissible continuous pulse load is determined by the resistance change as given for the extended endurance test (film temperature at 125 °C) after 8,000 hours.

Vishay



3. Pulse Voltage Limit

Resistors with resistance values above the critical value R_{crit} have to be protected against high pulse voltage. This is valid for single and continuous pulses. This limitation protects the resistor elements against high electrical field strength.

An example of the maximum permissible impulse voltage \hat{U}_{max} of professional thin film flat chip resistors is shown in Figure 4. For thin film resistors the permissible pulse voltage is determined by the resistance change as given for the extended endurance test (film temperature at 125 °C) after 8,000 hours.

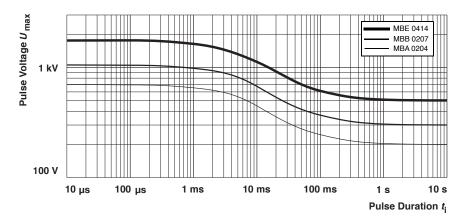


Figure 4. Maximum pulse voltage, single and continuous pulses.

4. Pulse Load Rating in Accordance with IEC 60115-1, 4.27

As mentioned in chapters 1, 2, and 3 the permissible pulse load depends on the shape and on the duration of the pulse. The standard lighting pulse (LEMP) is one of the main EMC test pulses for electronic and electrical equipment. The EN standard EN 60 115-1, clause 4.27 defines two different pulse generators for single high voltage pulses (source pulse):

- 1.2/50 µs
- 10/700 µs

The value shown before the slash is the front time T_1 in μ s of the pulse voltage and the figure after the slash is the time to half value T_2 of the peak value as shown in Figure 5.

The test conditions are:

- 5 pulses of 1.2/50 µs with a period of not less than 12 s
- 10 pulses of 10/700 µs with a period of not less than 1 min.

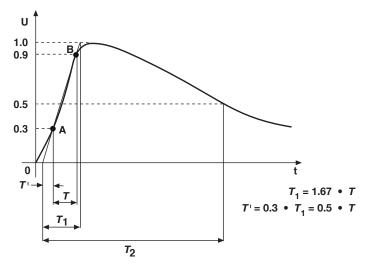


Figure 5. 1.2/50 µs or 10/700 µs single high voltage pulse.





Vishay

As a means of comparing different resistor styles and as a guiding value for development purposes, the pulse load capability in accordance with IEC 60 115-1, clause 4.27 may be applied. An example is the pulse load capability of a carbon film MINI-MELF resistor CMA 0204 as shown in Figure 6.

For thin film products the diagram gives the voltage limits resulting in the maximum resistance value change of 0.5%. Exceeding the limit may result in an exponential rise of the change and can introduce the destruction of the resistor.

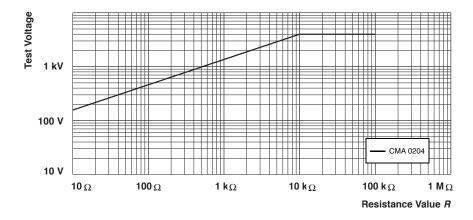


Figure 6. Source pulse capability of carbon film MINI-MELF resistors CMA 0204.

JRLDWIDE!

DISCRETE SEMICONDUCTORS AND PASSIVE COMPONENTS

THE AMERICAS VIS

VISHAY AMERICAS

ONE GREENWICH PLACE SHELTON, CT 06484 UNITED STATES PH: +1-402-563-6866 FAX: +1-402-563-6296

ASIA

VISHAY INTERTECHNOLOGY

ASIA PTE LTD.

25 TAMPINES STREET 92 KEPPEL BUILDING #02-00 SINGAPORE 528877 PH: +65-6788-6668 FAX: +65-6788-0988

JAPAN

VISHAY JAPAN CO., LTD.

GE EDISON BUILDING, SHIBUYA 3F

3-5-16 SHIBUYA SHIBUYA-KU TOKYO 150-0002 JAPAN

PH: +81-3-5464-6411 FAX: +81-3-5464-6433

EUROPE

VISHAY ELECTRONIC GMBH

GEHEIMRAT-ROSENTHAL-STR. 100

95100 SELB GERMANY

PH: +49-9287-71-0 FAX: +49-9287-70435

VISHAY S.A.

4, RUE DE SALONIQUE 95101 ARGENTEUIL

FRANCE

PH: +33-1-39-98-22-00 FAX: +33-1-39-98-22-05

VISHAY LTD.

PALLION INDUSTRIAL ESTATE SUNDERLAND, SR4 6SU GREAT BRITAIN

PH: +44-191-514-4155 FAX: +44-191-567-8262



ONE OF THE WORLD'S LARGEST MANUFACTURERS OF DISCRETE SEMICONDUCTORS AND PASSIVE COMPONENTS

Discrete Semiconductors: Diodes and Rectifiers • MOSFETs
• RF Transistors • Optoelectronics • Power ICs
Passive Components: Capacitors • Resistive Products • Magnetics
Integrated Modules: DC/DC Converters
Strain Sensors and Transducers: Strain Gages and Instruments
• PhotoStress*Instruments • Transducers

www.vishay.com

射频和天线设计培训课程推荐

易迪拓培训(www.edatop.com)由数名来自于研发第一线的资深工程师发起成立,致力并专注于微波、射频、天线设计研发人才的培养;我们于2006年整合合并微波EDA网(www.mweda.com),现已发展成为国内最大的微波射频和天线设计人才培养基地,成功推出多套微波射频以及天线设计经典培训课程和ADS、HFSS等专业软件使用培训课程,广受客户好评;并先后与人民邮电出版社、电子工业出版社合作出版了多本专业图书,帮助数万名工程师提升了专业技术能力。客户遍布中兴通讯、研通高频、埃威航电、国人通信等多家国内知名公司,以及台湾工业技术研究院、永业科技、全一电子等多家台湾地区企业。

易迪拓培训课程列表: http://www.edatop.com/peixun/rfe/129.html



射频工程师养成培训课程套装

该套装精选了射频专业基础培训课程、射频仿真设计培训课程和射频电路测量培训课程三个类别共 30 门视频培训课程和 3 本图书教材;旨在引领学员全面学习一个射频工程师需要熟悉、理解和掌握的专业知识和研发设计能力。通过套装的学习,能够让学员完全达到和胜任一个合格的射频工程师的要求…

课程网址: http://www.edatop.com/peixun/rfe/110.html

ADS 学习培训课程套装

该套装是迄今国内最全面、最权威的 ADS 培训教程, 共包含 10 门 ADS 学习培训课程。课程是由具有多年 ADS 使用经验的微波射频与通信系统设计领域资深专家讲解,并多结合设计实例,由浅入深、详细而又全面地讲解了 ADS 在微波射频电路设计、通信系统设计和电磁仿真设计方面的内容。能让您在最短的时间内学会使用 ADS, 迅速提升个人技术能力,把 ADS 真正应用到实际研发工作中去,成为 ADS 设计专家...



课程网址: http://www.edatop.com/peixun/ads/13.html



HFSS 学习培训课程套装

该套课程套装包含了本站全部 HFSS 培训课程,是迄今国内最全面、最专业的 HFSS 培训教程套装,可以帮助您从零开始,全面深入学习 HFSS 的各项功能和在多个方面的工程应用。购买套装,更可超值赠送 3 个月免费学习答疑,随时解答您学习过程中遇到的棘手问题,让您的 HFSS 学习更加轻松顺畅···

课程网址: http://www.edatop.com/peixun/hfss/11.html

CST 学习培训课程套装

该培训套装由易迪拓培训联合微波 EDA 网共同推出,是最全面、系统、 专业的 CST 微波工作室培训课程套装, 所有课程都由经验丰富的专家授 课,视频教学,可以帮助您从零开始,全面系统地学习 CST 微波工作的 各项功能及其在微波射频、天线设计等领域的设计应用。且购买该套装, 还可超值赠送3个月免费学习答疑…







HFSS 天线设计培训课程套装

套装包含6门视频课程和1本图书,课程从基础讲起,内容由浅入深, 理论介绍和实际操作讲解相结合,全面系统的讲解了 HFSS 天线设计的 全过程。是国内最全面、最专业的 HFSS 天线设计课程,可以帮助您快 速学习掌握如何使用 HFSS 设计天线, 让天线设计不再难…

课程网址: http://www.edatop.com/peixun/hfss/122.html

13.56MHz NFC/RFID 线圈天线设计培训课程套装

套装包含 4 门视频培训课程,培训将 13.56MHz 线圈天线设计原理和仿 真设计实践相结合,全面系统地讲解了13.56MHz线圈天线的工作原理、 设计方法、设计考量以及使用 HFSS 和 CST 仿真分析线圈天线的具体 操作,同时还介绍了 13.56MHz 线圈天线匹配电路的设计和调试。通过 该套课程的学习,可以帮助您快速学习掌握 13.56MHz 线圈天线及其匹 配电路的原理、设计和调试…



详情浏览: http://www.edatop.com/peixun/antenna/116.html

我们的课程优势:

- ※ 成立于 2004年, 10 多年丰富的行业经验,
- ※ 一直致力并专注于微波射频和天线设计工程师的培养,更了解该行业对人才的要求
- ※ 经验丰富的一线资深工程师讲授,结合实际工程案例,直观、实用、易学

联系我们:

- ※ 易迪拓培训官网: http://www.edatop.com
- ※ 微波 EDA 网: http://www.mweda.com
- ※ 官方淘宝店: http://shop36920890.taobao.com

易迪拓信训 官方网址: http://www.edatop.com