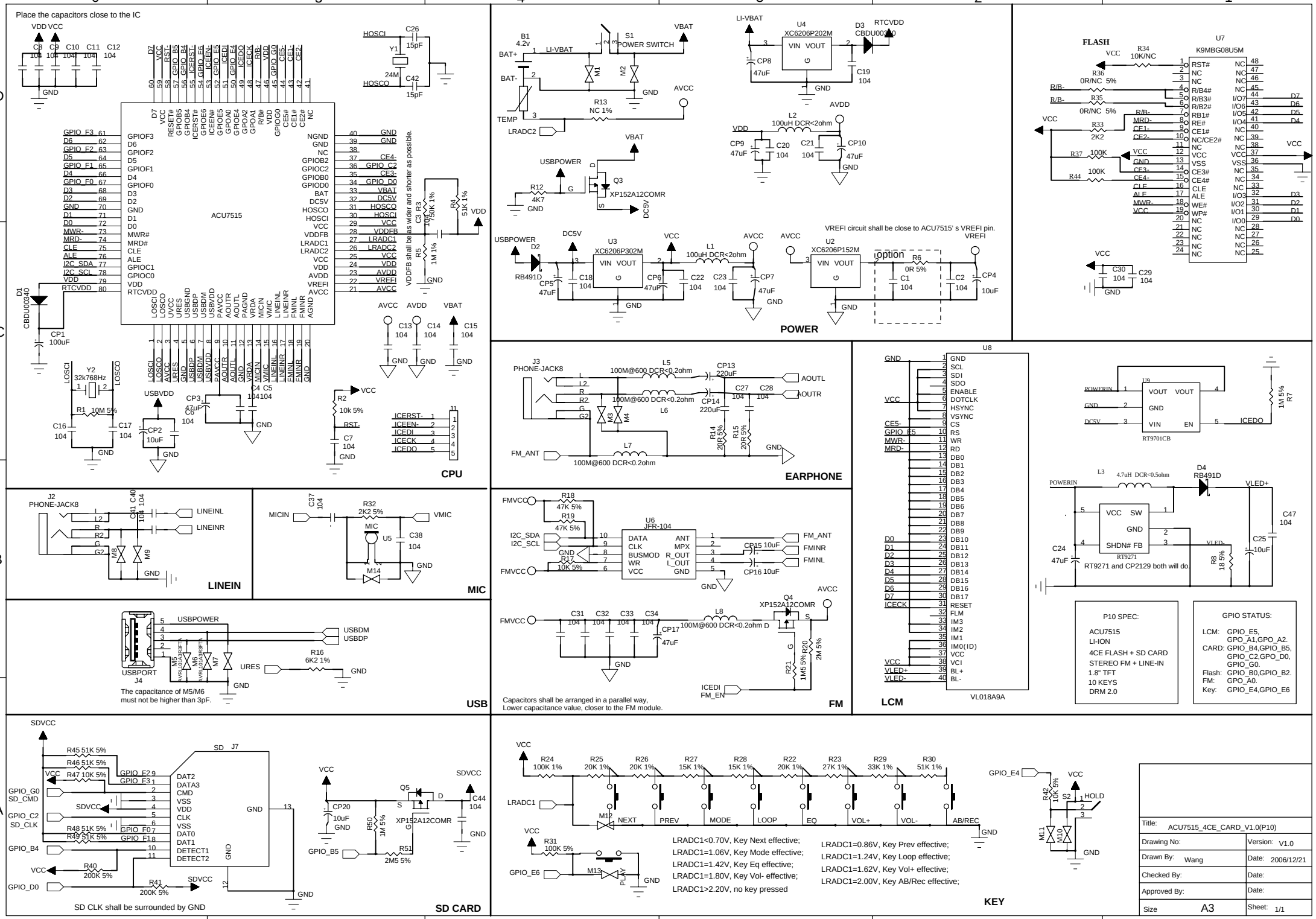
Place the capacitors close to the IC
ACU7515
CPU
POWER
FLASH
K9MBG08U5M
XC6206P202M
XC6206P302M
XC6206P152M
XP152A12COMR
VREFI circuit shall be close to ACU7515' s VREFI pin.
option
EARPHONE
LINEIN
MIC
USB
The capacitance of M5/M6 must not be higher than 3pF.
FM
Capacitors shall be arranged in a parallel way, Lower capacitance value, closer to the FM module.
LCM
VL018A9A
RT9701CB
RT9271
RT9271 and CP2129 both will do.
P10 SPEC:
ACU7515
LI-ION
4CE FLASH + SD CARD
STEREO FM + LINE-IN
1.8" TFT
10 KEYS
DRM 2.0
GPIO STATUS:
LCM: GPIO_E5, GPO_A1,GPO_A2.
CARD: GPIO_B4,GPIO_B5, GPIO_C2,GPO_D0, GPIO_G0.
Flash: GPIO_B0,GPIO_B2.
FM: GPO_A0.
Key: GPIO_E4,GPIO_E6
SD CLK shall be surrounded by GND
SD CARD
LRADC1<0.70V, Key Next effective;
LRADC1=1.06V, Key Mode effective;
LRADC1=1.42V, Key Eq effective;
LRADC1=1.80V, Key Vol- effective;
LRADC1>2.20V, no key pressed
LRADC1=0.86V, Key Prev effective;
LRADC1=1.24V, Key Loop effective;
LRADC1=1.62V, Key Vol+ effective;
LRADC1=2.00V, Key AB/Rec effective;
KEY
Title: ACU7515_4CE_CARD_V1.0(P10)
Drawing No:
Version: V1.0
Drawn By: Wang
Date: 2006/12/21
Checked By:
Date:
Approved By:
Date:
Size A3
Sheet: 1/1